

JOB VACANCY ANNOUNCEMENT

We are looking for post-docs (or equivalent experience) with experience in the design of high-frequency analog-to-digital converters

IMSE Sigma-Delta Research Group is searching for postdocs (or equivalent experience) to join us in some of our research projects dealing with the design of (Sigma-Delta) Analog-to-Digital Converters with application in Edge-AI, Software-Defined-Radio and AIoT.

Requirements / We expect:

- Strong background in EE engineering, Microelectronics, Physics and related areas. Expertise in the following topics: Design and verification of analog and mixed-signal integrated circuits (amplifiers, filters, comparators, ..); design of high-speed Sigma-Delta analog-to-digital converters; analog design flow in nanometer CMOS. Use of CAD tools (Cadence IC design, Synopsys).
- Prior experience in high-speed Sigma-Delta ADCs is a plus
- EU citizenship or EU residence permission recommended.

Conditions of the post / We offer:

- Duration: 3 years
- Gross salary: ~3000€/month (~35k€/year)
- Workplace: Instituto de Microelectrónica de Sevilla (IMSE-CNM) is a joint institute of the Consejo Superior de Investigaciones Científicas and the University of Seville.

Submission procedure

Interested candidates should submit their applications through the following website:

<https://investigacion.us.es/sisius/investigador?setlang=en>

(job offer INV-IND-09-2025-I-008)

Instructions to complete the submission available [here](#).

Deadline: September 19, 2025

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